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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/731,069	12/09/2003	Pravinkumar Premakanthan	1280-SC12966ZC	3764

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EXAMINER

CHU, MICHAEL

ART UNIT PAPER NUMBER

2618

DATE MAILED: 07/31/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

**Advisory Action
Before the Filing of an Appeal Brief**

Application No.

10/731,069

Applicant(s)

PREMAKANTHAN ET AL.

Examiner

Michael Chu

Art Unit

2618

-The MAILING DATE of this communication appears on the cover sheet with the correspondence address -

THE REPLY FILED 03 July 2006 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☒ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.
b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. ☐ The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. ☐ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
(a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);
(b) ☐ They raise the issue of new matter (see NOTE below);
(c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
(d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
5. ☐ Applicant's reply has overcome the following rejection(s): _____.
6. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
7. ☐ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.
The status of the claim(s) is (or will be) as follows:
Claim(s) allowed: _____.
Claim(s) objected to: _____.
Claim(s) rejected: _____.
Claim(s) withdrawn from consideration: _____.

AFFIDAVIT OR OTHER EVIDENCE

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See Continuation Sheet.
12. ☐ Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s). _____.
13. ☐ Other: _____.

Continuation of 11. The request for reconsideration has been considered but does NOT place the application in condition for allowance because: examiner respectfully disagrees with arguments and stands on 102 rejections.

Response to Arguments

The reply filed on July 3, 2006 is have been fully considered but they are not persuasive. In response to applicant's argument in claim 17 that reference fails to teach, mention, disclose or suggest amplifying the RF signal with a gain control stage to produce an amplified signal, and detecting a ramp of the amplified signal.

Examiner respectfully disagrees, and further cited in Epperson et al. (US Publication# 2003/0040343) discloses "an adjustable power control signal 74 (Vramp) may be received by a negative input 76 of an operational amplifier forming error amplifier 68. The output 78 of the voltage regulator 70 is fed back through the feedback network 72 and received by positive input 80 of error amplifier 68. An output signal 82 from error amplifier 68 is provided to a control input 84 of the voltage regulator that controls the regulated output 78 of voltage regulator 70. The voltage regulator 70 regulates the voltage supplied to the rails 86, 88 of the second and third amplifier stages 62, 64, respectively. These rails 86, 88 will typically be the collectors or drains of bipolar or field effect transistors forming the respective amplifier stages" (0035, See Parts 74, 76, 68, 78, 70, 72, 80, 82, 84, 86, 88, 62, 64 of Figure 2).

Epperson et al. teaches/mentions an amplified RF signal (0031, lines 1-11, 0034, lines 1-9, See Parts 60, 62, 64, 46, 44, 74 of Figure 2, See Part 32 of Figure 1), where the power amplifier circuitry 44 provides gain for the signal to be transmitted under control of the power control circuitry 46, which is preferably controlled by the control system 32 using the Vramp signal 74. The ramp up time and ramp down time of the adjustable power control signal (Vramp) must conform to the shape of the burst mask, where the amplitude of Vramp dictates the output power (0012, lines 1-11, 0014, See Parts 46, 74 of Figure 2, 0034-0035).

Therefore, examiner interpreted "amplifying the RF signal with a gain control stage to produce an amplified signal", and "detecting a ramp of the amplified signal" as broadest reasonable interpretation, and it is proper. Claims 18-24 and 26-30 depend on independent claim 17 above and are anticipated by Epperson et al. for the reasons stated above under base claim 17. These claims are not in condition for allowance.

In response to applicant's argument in claim 1 that reference fails to teach, mention, disclose or suggest a power detector coupled to an output of the gain control stage, the power detector to detect a ramp of the amplified signal and to provide an indication of the ramp.

Examiner respectfully disagrees, and further cited in Epperson et al. (US Publication# 2003/0040343) teaches/mentions an adjustable power control signal 74 (Vramp) and a voltage regulator 70 within the power control circuitry 46 (0034, lines 1-9, 0035, lines 1-14, See Parts 74, 70, 78, 46 of Figure 2), where the voltage regulator controls the regulated output 78. The same regulated output 78 is regulates voltage supplied to the second and third amplifier stages 62, 64, respectively of the power amplifier circuitry 44 (0035, lines 1-14, particularly lines 7-11, See Parts 78, 62, 64, 44 of Figure 2).

Epperson et al. shows an amplified RF signal (0031, lines 1-11, 0034, lines 1-9, See Parts 60, 62, 64, 46, 44, 74 of Figure 2, See Part 32 of Figure 1), where the power amplifier circuitry 44 provides gain for the signal to be transmitted under control of the power control circuitry 46, which is preferably controlled by the control system 32 using the Vramp signal 74. The ramp up time and ramp down time of the adjustable power control signal (Vramp) must conform to the shape of the burst mask, where the amplitude of Vramp dictates the output power (0012, lines 1-11, 0014, See Parts 46, 74 of Figure 2, 0035).

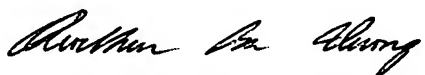
Therefore, examiner interpreted "a power detector coupled to an output of the gain control stage, the power detector to detect a ramp of the amplified signal and to provide an indication of the ramp" as broadest reasonable interpretation, and it is proper. Claims 2-10 and 12-16 depend on independent claim 1 and are anticipated by Epperson et al. for the reasons stated above under base claim 1. Thus, these claims are not in condition for allowance.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael Chu whose telephone number is 571-272-7875. The examiner can normally be reached on Monday-Friday (8:30am-5pm). If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nay Maung can be reached on 571-272-7882. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Michael Chu
Examiner
Art Unit 2618

MC 07/17/2006

 7/20/06

QUOCHIEN B. VUONG
PRIMARY EXAMINER